

REMARKS

This Amendment D is in response to the Non-Final Office Action of July 12, 2007 in which claims 1-3, 5-10, 14, 15, 17, 19-33, 36 and 37 were rejected and claims 4 and 18 were objected to.

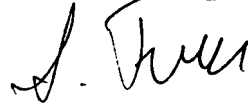
In the present Office Action, the Examiner repeated the same rejection as in the previous Office Action of May 7, 2007. The Applicant continues strongly disagree with the rejection and refer to the arguments present in the communication to the USPTO on June 7, 2007 which are fully applied here.

In the response to arguments Section of the Office Action of July 12, 2007, the Examiner stated: "Therefore, the triggering signal is activated by a read operation, write operation, or a clock pulse instead of for performing those purposes or functions. This is totally inaccurate. Nowhere in the application it is stated that "the triggering signal is activated by a read operation, write operation, or a clock pulse instead of for performing those purposes or functions", as alleged by the Examiner. On the contrary, these operations are performed as normal (e.g., see an arrow of a triggering signal 26 towards the multi-element memory 10 in Figure 3 of the present patent application).

To further clarify this major misunderstanding of the present invention by the Office, the independent claims 1, 20 and 32 are further amended, as submitted herein, to include a clarification "and wherein said triggering signal is for implementing both said event and said memory leveling".

The objections and rejections of the Office Action of July 12, 2007 having been obviated by amendment or shown to be inapplicable, withdrawal thereof is requested and passage of all claims to issue is solicited. Consideration and allowance are respectively requested.

Respectfully submitted,



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